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Title BLOCK DIAGRAM			
Size Custom	Document Number IX58A-AXP		Rev 5.3
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Title		POWER DELIVERY	
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Title CLOCK DISTRIBUTION			
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REVISION HISTORY			
Title			
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12 CPU1A <<M_DATA_A[B3..0] 12 <<M_DQS_A_DP[B..0] 12 <<M_DQS_A_DN[B..0] 12

M_DATA_A0 W41	DDR0_DQ_0	T43 M_DQS_A_DP0
M_DATA_A1 V41	DDR0_DQ_1	U43 M_DQS_A_DN0
M_DATA_A2 R43	DDR0_DQ_2	
M_DATA_A3 R42	DDR0_DQ_3	L41 M_DQS_A_DP1
M_DATA_A4 W40	DDR0_DQ_4	M41 M_DQS_A_DN1
M_DATA_A5 W42	DDR0_DQ_5	
M_DATA_A6 U41	DDR0_DQ_6	F41 M_DQS_A_DP2
M_DATA_A7 T42	DDR0_DQ_7	G41 M_DQS_A_DN2
M_DATA_A8 N41	DDR0_DQ_8	
M_DATA_A9 N43	DDR0_DQ_9	B39 M_DQS_A_DP3
M_DATA_A10 K42	DDR0_DQ_10	B40 M_DQS_A_DN3
M_DATA_A11 K43	DDR0_DQ_11	
M_DATA_A12 P42	DDR0_DQ_12	E3 M_DQS_A_DP4
M_DATA_A13 P41	DDR0_DQ_13	E4 M_DQS_A_DN4
M_DATA_A14 L43	DDR0_DQ_14	
M_DATA_A15 L42	DDR0_DQ_15	K2 M_DQS_A_DP5
M_DATA_A16 H41	DDR0_DQ_16	K3 M_DQS_A_DN5
M_DATA_A17 H43	DDR0_DQ_17	
M_DATA_A18 E42	DDR0_DQ_18	R2 M_DQS_A_DP6
M_DATA_A19 E43	DDR0_DQ_19	R3 M_DQS_A_DN6
M_DATA_A20 J42	DDR0_DQ_20	
M_DATA_A21 J41	DDR0_DQ_21	W2 M_DQS_A_DP7
M_DATA_A22 F43	DDR0_DQ_22	W1 M_DQS_A_DN7
M_DATA_A23 F42	DDR0_DQ_23	
M_DATA_A24 D40	DDR0_DQ_24	D34 M_DQS_A_DP8
M_DATA_A25 C41	DDR0_DQ_25	D35 M_DQS_A_DN8
M_DATA_A26 A38	DDR0_DQ_26	
M_DATA_A27 D37	DDR0_DQ_27	V43
M_DATA_A28 D37	DDR0_DQ_28	V42
M_DATA_A29 D42	DDR0_DQ_29	
M_DATA_A30 C38	DDR0_DQ_30	N42
M_DATA_A31 B38	DDR0_DQ_31	M43
M_DATA_A32 B5	DDR0_DQ_32	
M_DATA_A33 C4	DDR0_DQ_33	H42
M_DATA_A34 F1	DDR0_DQ_34	G43
M_DATA_A35 G3	DDR0_DQ_35	
M_DATA_A36 B6	DDR0_DQ_36	D39
M_DATA_A37 C6	DDR0_DQ_37	C39
M_DATA_A38 F3	DDR0_DQ_38	
M_DATA_A39 F2	DDR0_DQ_39	D5
M_DATA_A40 H2	DDR0_DQ_40	D4
M_DATA_A41 H1	DDR0_DQ_41	
M_DATA_A42 L1	DDR0_DQ_42	J2
M_DATA_A43 M1	DDR0_DQ_43	J1
M_DATA_A44 G1	DDR0_DQ_44	
M_DATA_A45 H3	DDR0_DQ_45	P2
M_DATA_A46 L3	DDR0_DQ_46	P1
M_DATA_A47 L2	DDR0_DQ_47	
M_DATA_A48 N1	DDR0_DQ_48	V2
M_DATA_A49 N2	DDR0_DQ_49	V3
M_DATA_A50 T1	DDR0_DQ_50	
M_DATA_A51 T2	DDR0_DQ_51	B36
M_DATA_A52 M3	DDR0_DQ_52	B35
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M_DATA_A54 T3	DDR0_DQ_54	
M_DATA_A55 U4	DDR0_DQ_55	
M_DATA_A56 U4	DDR0_DQ_56	
M_DATA_A57 Y1	DDR0_DQ_57	
M_DATA_A58 Y2	DDR0_DQ_58	
M_DATA_A59 Y3	DDR0_DQ_59	
M_DATA_A60 U1	DDR0_DQ_60	
M_DATA_A61 U3	DDR0_DQ_61	
M_DATA_A62 V4	DDR0_DQ_62	
M_DATA_A63 W4	DDR0_DQ_63	
M_CB_ECC_A0 C36	DDR0_ECC_0	
M_CB_ECC_A1 A36	DDR0_ECC_1	
M_CB_ECC_A2 F32	DDR0_ECC_2	
M_CB_ECC_A3 C33	DDR0_ECC_3	
M_CB_ECC_A4 C37	DDR0_ECC_4	
M_CB_ECC_A5 A37	DDR0_ECC_5	
M_CB_ECC_A6 B34	DDR0_ECC_6	
M_CB_ECC_A7 C34	DDR0_ECC_7	

LGA 1366 SOCKET <<M_CB_ECC_A[7..0] 12

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M_DATA_B0 AA37	DDR1_DQ_0	Y38 M_DQS_B_DP0
M_DATA_B1 AA36	DDR1_DQ_1	Y37 M_DQS_B_DN0
M_DATA_B2 Y35	DDR1_DQ_2	
M_DATA_B3 Y34	DDR1_DQ_3	R38 M_DQS_B_DP1
M_DATA_B4 AA35	DDR1_DQ_4	R37 M_DQS_B_DN1
M_DATA_B5 AB36	DDR1_DQ_5	
M_DATA_B6 Y40	DDR1_DQ_6	L35 M_DQS_B_DP2
M_DATA_B7 Y39	DDR1_DQ_7	L36 M_DQS_B_DN2
M_DATA_B8 P34	DDR1_DQ_8	
M_DATA_B9 P35	DDR1_DQ_9	L30 M_DQS_B_DP3
M_DATA_B10 P39	DDR1_DQ_10	L31 M_DQS_B_DN3
M_DATA_B11 N39	DDR1_DQ_11	
M_DATA_B12 R34	DDR1_DQ_12	E7 M_DQS_B_DP4
M_DATA_B13 R35	DDR1_DQ_13	D7 M_DQS_B_DN4
M_DATA_B14 N37	DDR1_DQ_14	
M_DATA_B15 N38	DDR1_DQ_15	H6 M_DQS_B_DP5
M_DATA_B16 M35	DDR1_DQ_16	G6 M_DQS_B_DN5
M_DATA_B17 M34	DDR1_DQ_17	
M_DATA_B18 K35	DDR1_DQ_18	L6 M_DQS_B_DP6
M_DATA_B19 J35	DDR1_DQ_19	L5 M_DQS_B_DN6
M_DATA_B20 N34	DDR1_DQ_20	
M_DATA_B21 M36	DDR1_DQ_21	Y8 M_DQS_B_DP7
M_DATA_B22 J36	DDR1_DQ_22	Y9 M_DQS_B_DN7
M_DATA_B23 H36	DDR1_DQ_23	
M_DATA_B24 L33	DDR1_DQ_24	G33 M_DQS_B_DP8
M_DATA_B25 K32	DDR1_DQ_25	G34 M_DQS_B_DN8
M_DATA_B26 K32	DDR1_DQ_26	
M_DATA_B27 J32	DDR1_DQ_27	AA40
M_DATA_B28 J34	DDR1_DQ_28	AA41
M_DATA_B29 H34	DDR1_DQ_29	
M_DATA_B30 L32	DDR1_DQ_30	P36
M_DATA_B31 K30	DDR1_DQ_31	P37
M_DATA_B32 E8	DDR1_DQ_32	
M_DATA_B33 E8	DDR1_DQ_33	K37
M_DATA_B34 E5	DDR1_DQ_34	
M_DATA_B35 F5	DDR1_DQ_35	K34
M_DATA_B36 F10	DDR1_DQ_36	K33
M_DATA_B37 F8	DDR1_DQ_37	
M_DATA_B38 D6	DDR1_DQ_38	F8
M_DATA_B39 F6	DDR1_DQ_39	F7
M_DATA_B40 H6	DDR1_DQ_40	
M_DATA_B41 J6	DDR1_DQ_41	H2
M_DATA_B42 G4	DDR1_DQ_42	J7
M_DATA_B43 H4	DDR1_DQ_43	
M_DATA_B44 G4	DDR1_DQ_44	M5
M_DATA_B45 E5	DDR1_DQ_45	M4
M_DATA_B46 J5	DDR1_DQ_46	
M_DATA_B47 K4	DDR1_DQ_47	Y4
M_DATA_B48 K4	DDR1_DQ_48	Y5
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M_DATA_B54 R8	DDR1_DQ_54	
M_DATA_B55 R7	DDR1_DQ_55	
M_DATA_B56 W6	DDR1_DQ_56	
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M_DATA_B58 Y10	DDR1_DQ_58	
M_DATA_B59 W10	DDR1_DQ_59	
M_DATA_B60 V9	DDR1_DQ_60	
M_DATA_B61 V5	DDR1_DQ_61	
M_DATA_B62 AA7	DDR1_DQ_62	
M_DATA_B63 W9	DDR1_DQ_63	
M_CB_ECC_B0 D36	DDR1_ECC_0	
M_CB_ECC_B1 F36	DDR1_ECC_1	
M_CB_ECC_B2 E33	DDR1_ECC_2	
M_CB_ECC_B3 G36	DDR1_ECC_3	
M_CB_ECC_B4 F37	DDR1_ECC_4	
M_CB_ECC_B5 F37	DDR1_ECC_5	
M_CB_ECC_B6 E34	DDR1_ECC_6	
M_CB_ECC_B7 G35	DDR1_ECC_7	

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M_DATA_C1 W35	DDR2_DQ_1	W36 M_DQS_C_DN0
M_DATA_C2 V36	DDR2_DQ_2	
M_DATA_C3 U36	DDR2_DQ_3	T37 M_DQS_C_DP1
M_DATA_C4 U34	DDR2_DQ_4	T38 M_DQS_C_DN1
M_DATA_C5 V34	DDR2_DQ_5	
M_DATA_C6 V37	DDR2_DQ_6	K40 M_DQS_C_DP2
M_DATA_C7 V38	DDR2_DQ_7	K39 M_DQS_C_DN2
M_DATA_C8 U38	DDR2_DQ_8	
M_DATA_C9 U39	DDR2_DQ_9	E39 M_DQS_C_DP3
M_DATA_C10 R39	DDR2_DQ_10	E40 M_DQS_C_DN3
M_DATA_C11 T36	DDR2_DQ_11	
M_DATA_C12 V39	DDR2_DQ_12	J10 M_DQS_C_DP4
M_DATA_C13 V39	DDR2_DQ_13	J9 M_DQS_C_DN4
M_DATA_C14 T41	DDR2_DQ_14	
M_DATA_C15 R40	DDR2_DQ_15	L7 M_DQS_C_DP5
M_DATA_C16 M39	DDR2_DQ_16	K7 M_DQS_C_DN5
M_DATA_C17 M40	DDR2_DQ_17	
M_DATA_C18 J40	DDR2_DQ_18	P6 M_DQS_C_DP6
M_DATA_C19 J39	DDR2_DQ_19	P5 M_DQS_C_DN6
M_DATA_C20 F40	DDR2_DQ_20	
M_DATA_C21 N38	DDR2_DQ_21	U8 M_DQS_C_DP7
M_DATA_C22 L40	DDR2_DQ_22	T8 M_DQS_C_DN7
M_DATA_C23 K38	DDR2_DQ_23	
M_DATA_C24 F40	DDR2_DQ_24	G29 M_DQS_C_DP8
M_DATA_C25 F40	DDR2_DQ_25	G30 M_DQS_C_DN8
M_DATA_C26 J37	DDR2_DQ_26	
M_DATA_C27 H37	DDR2_DQ_27	U35
M_DATA_C28 H39	DDR2_DQ_28	T35
M_DATA_C29 G39	DDR2_DQ_29	
M_DATA_C30 F38	DDR2_DQ_30	U40
M_DATA_C31 E38	DDR2_DQ_31	T40
M_DATA_C32 K12	DDR2_DQ_32	
M_DATA_C33 H13	DDR2_DQ_33	M38
M_DATA_C34 H13	DDR2_DQ_34	L38
M_DATA_C35 L13	DDR2_DQ_35	
M_DATA_C36 G11	DDR2_DQ_36	H39
M_DATA_C37 G10	DDR2_DQ_37	G38
M_DATA_C38 H12	DDR2_DQ_38	
M_DATA_C39 L12	DDR2_DQ_39	H11
M_DATA_C40 L10	DDR2_DQ_40	J11
M_DATA_C41 K10	DDR2_DQ_41	
M_DATA_C42 M9	DDR2_DQ_42	K9
M_DATA_C43 N9	DDR2_DQ_43	K8
M_DATA_C44 L11	DDR2_DQ_44	
M_DATA_C45 M10	DDR2_DQ_45	N4
M_DATA_C46 L8	DDR2_DQ_46	P4
M_DATA_C47 M8	DDR2_DQ_47	
M_DATA_C48 P7	DDR2_DQ_48	V6
M_DATA_C49 N6	DDR2_DQ_49	V7
M_DATA_C50 P9	DDR2_DQ_50	
M_DATA_C51 P10	DDR2_DQ_51	H31
M_DATA_C52 N7	DDR2_DQ_52	G31
M_DATA_C53 N9	DDR2_DQ_53	
M_DATA_C54 R10	DDR2_DQ_54	
M_DATA_C55 R9	DDR2_DQ_55	
M_DATA_C56 U9	DDR2_DQ_56	
M_DATA_C57 U9	DDR2_DQ_57	
M_DATA_C58 T10	DDR2_DQ_58	
M_DATA_C59 U10	DDR2_DQ_59	
M_DATA_C60 T7	DDR2_DQ_60	
M_DATA_C61 T9	DDR2_DQ_61	
M_DATA_C62 V8	DDR2_DQ_62	
M_DATA_C63 U8	DDR2_DQ_63	
M_CB_ECC_C0 H32	DDR2_ECC_0	
M_CB_ECC_C1 F33	DDR2_ECC_1	
M_CB_ECC_C2 E29	DDR2_ECC_2	
M_CB_ECC_C3 E30	DDR2_ECC_3	
M_CB_ECC_C4 J31	DDR2_ECC_4	
M_CB_ECC_C5 J30	DDR2_ECC_5	
M_CB_ECC_C6 F31	DDR2_ECC_6	
M_CB_ECC_C7 F30	DDR2_ECC_7	

LGA 1366 SOCKET <<M_CB_ECC_C[7..0] 14



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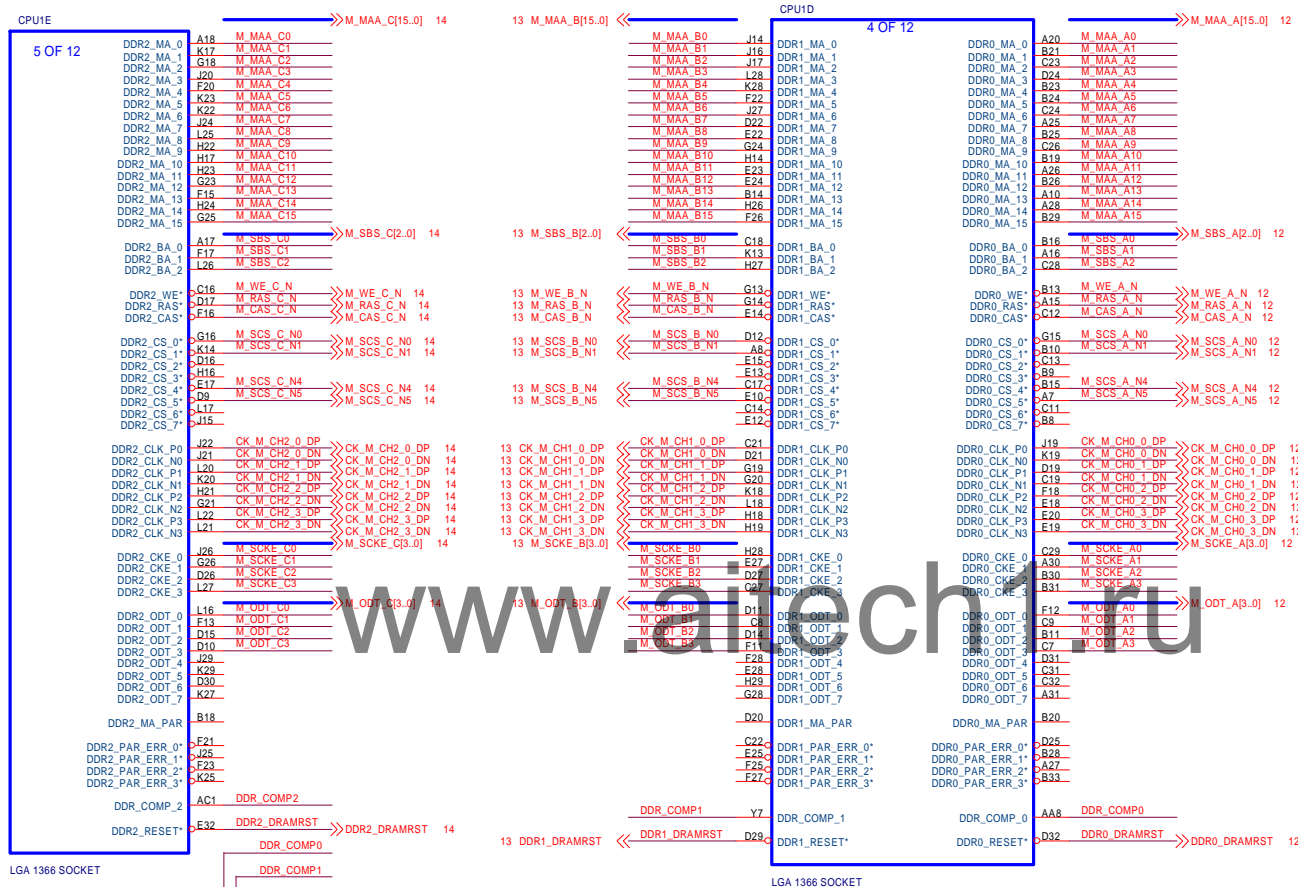
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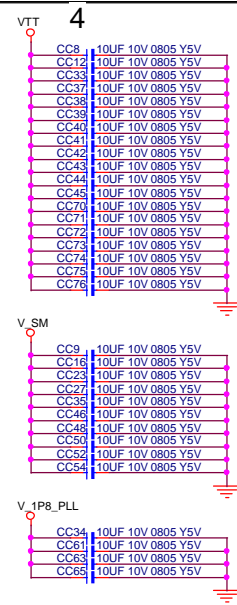
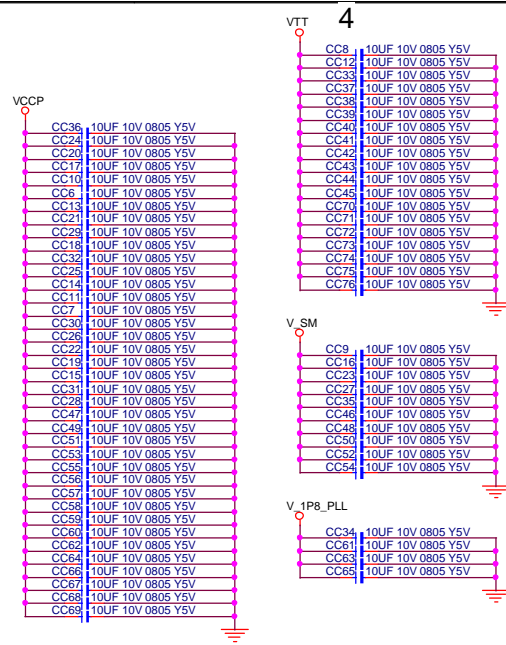
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Title SKTB 1366 Intel CPU-2			
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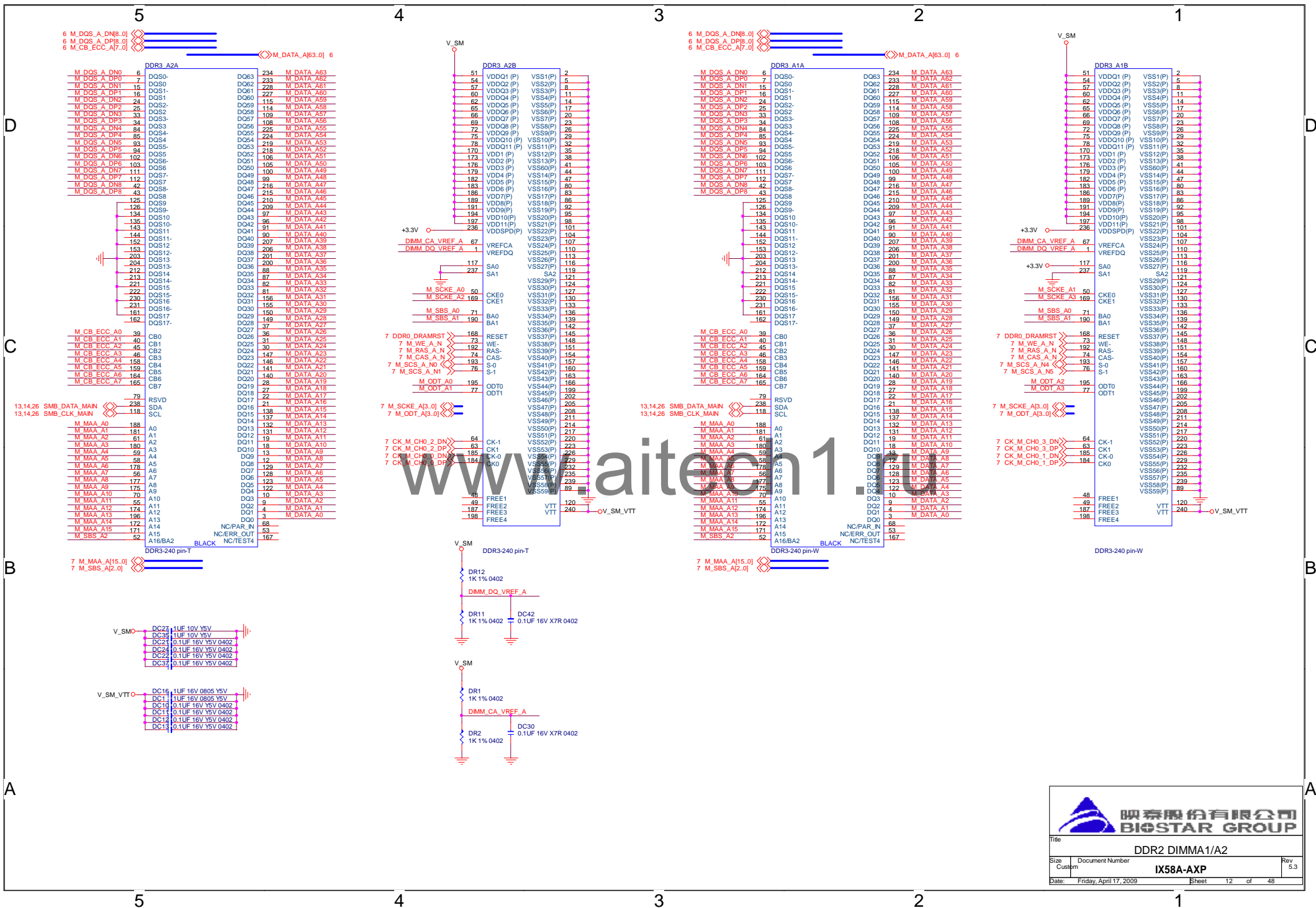


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Title CPU CAP		
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Title DDR ADD/CTL/VTT TER		
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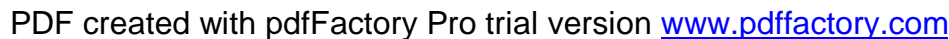


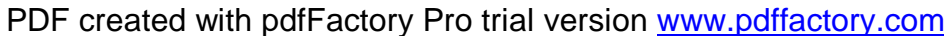
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Title: **DDR2 DIMM1A/A2**

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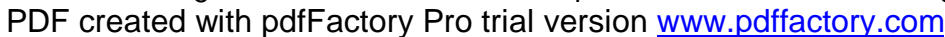
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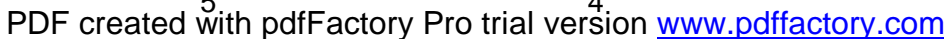
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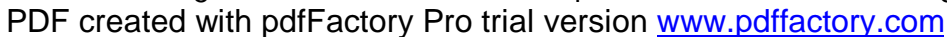
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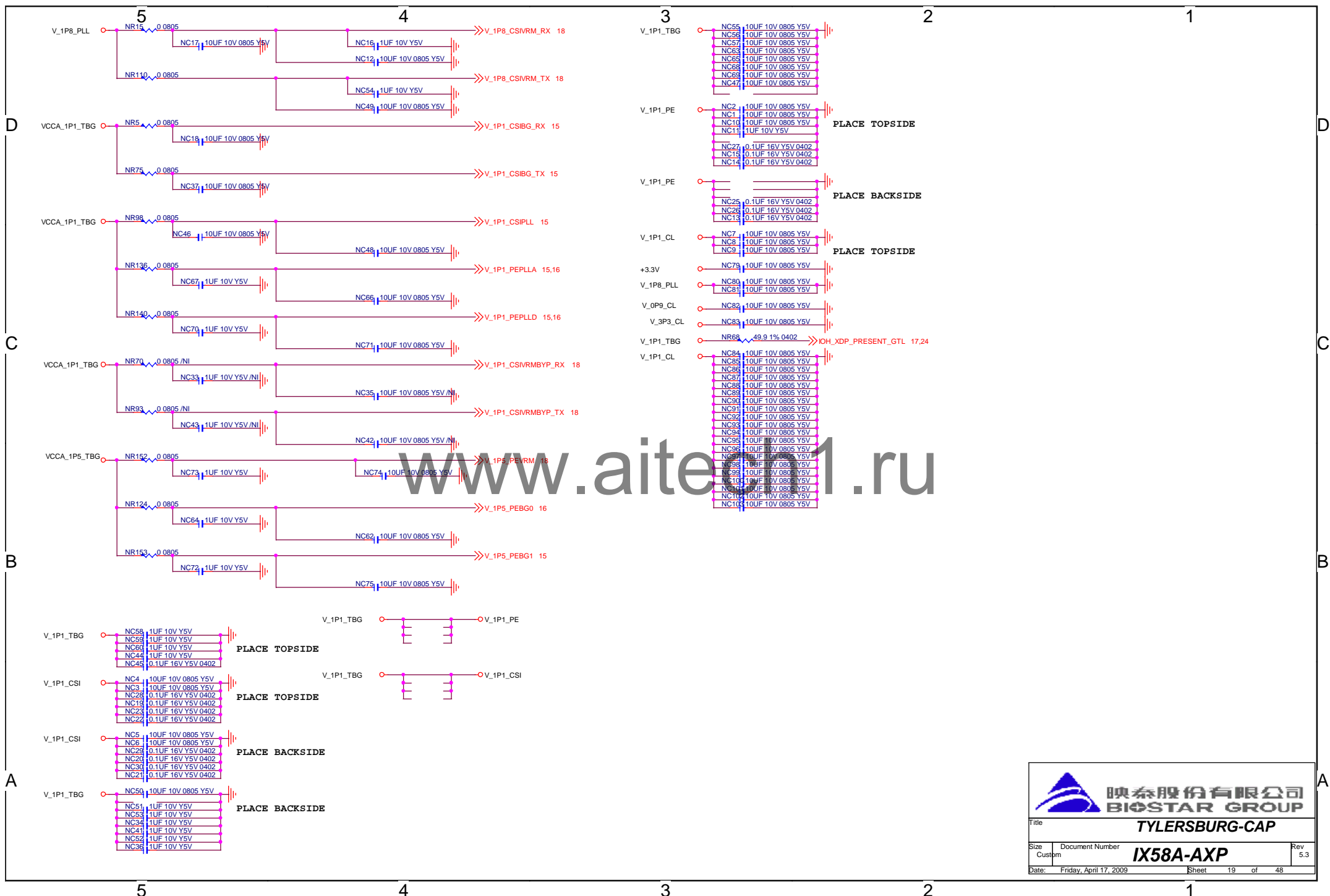
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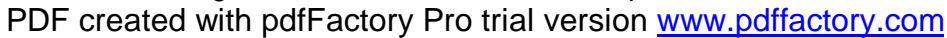
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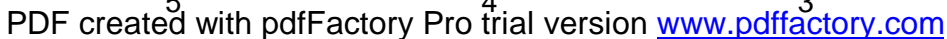






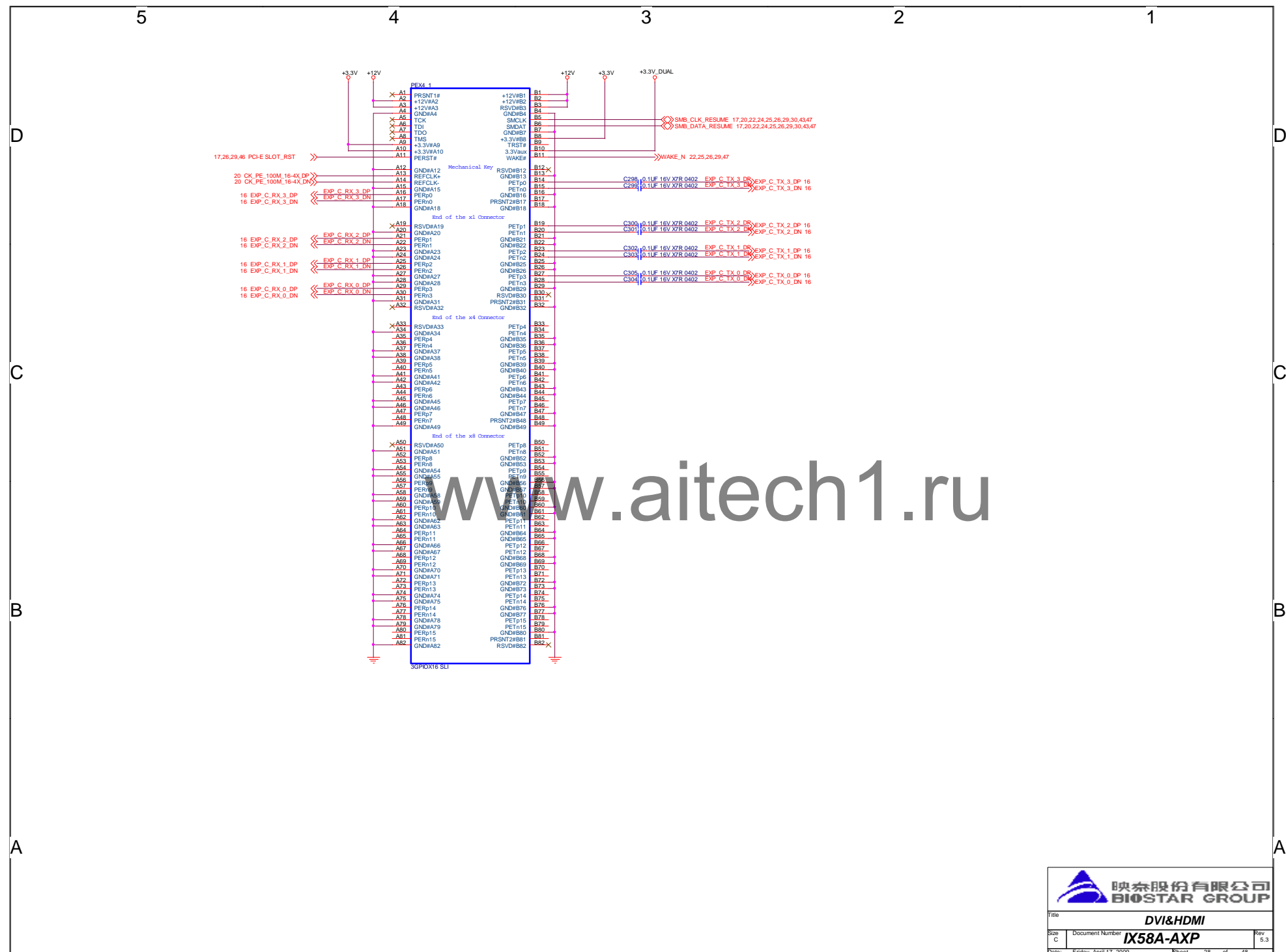




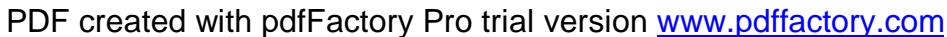


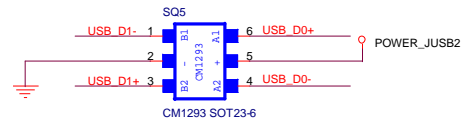
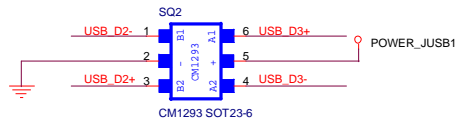




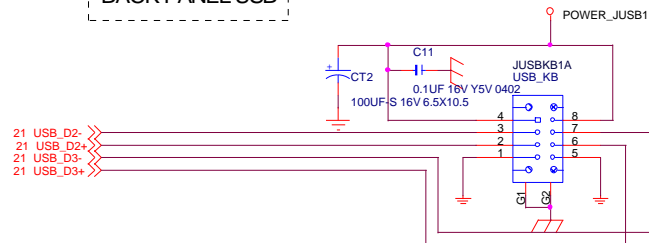




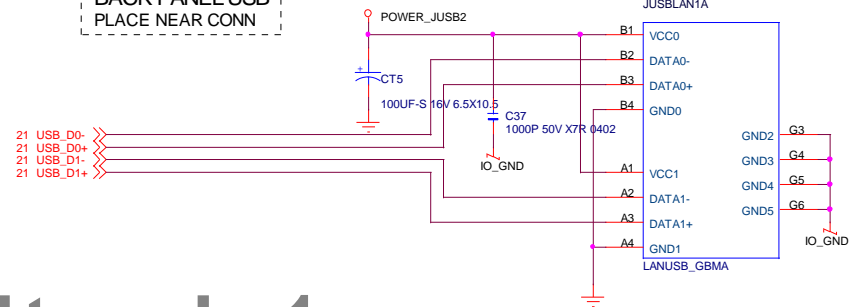




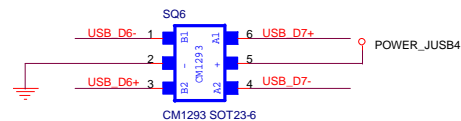
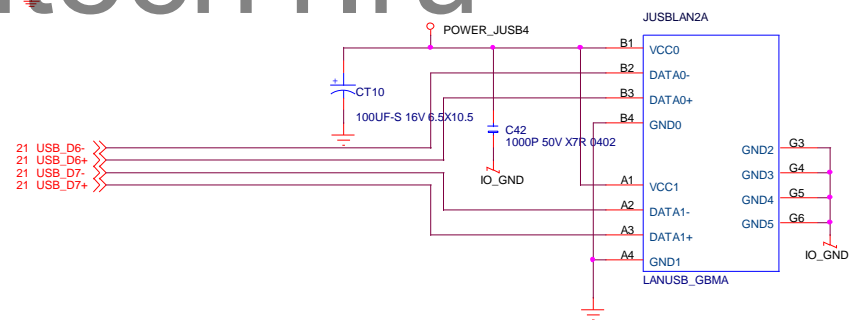
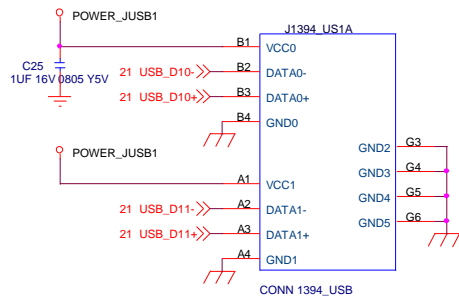
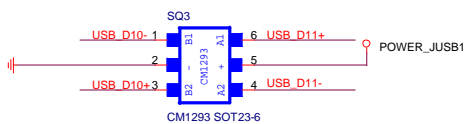
BACK PANEL USB



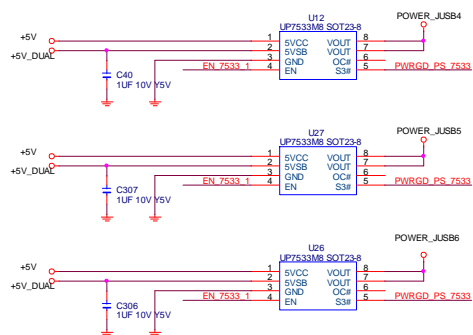
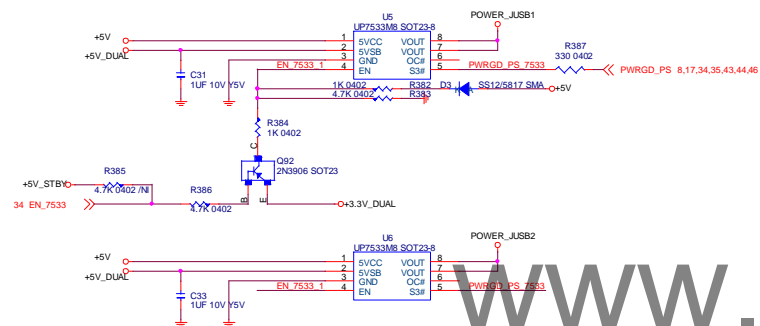
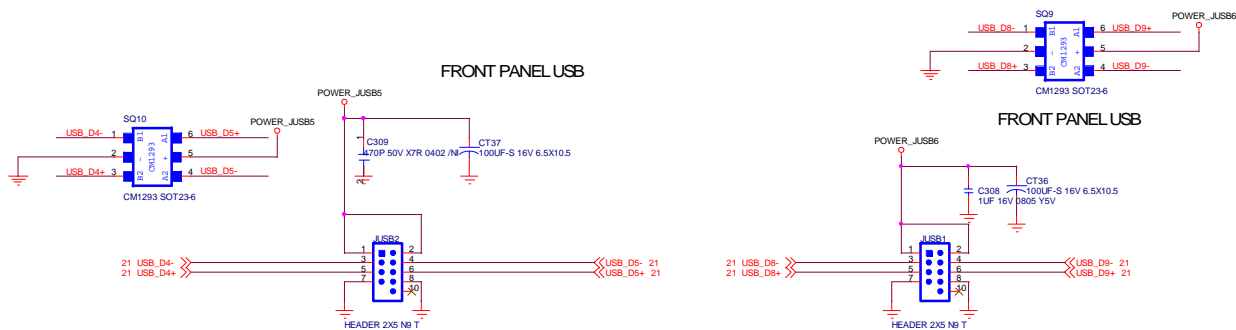
BACK PANEL USB
PLACE NEAR CONN

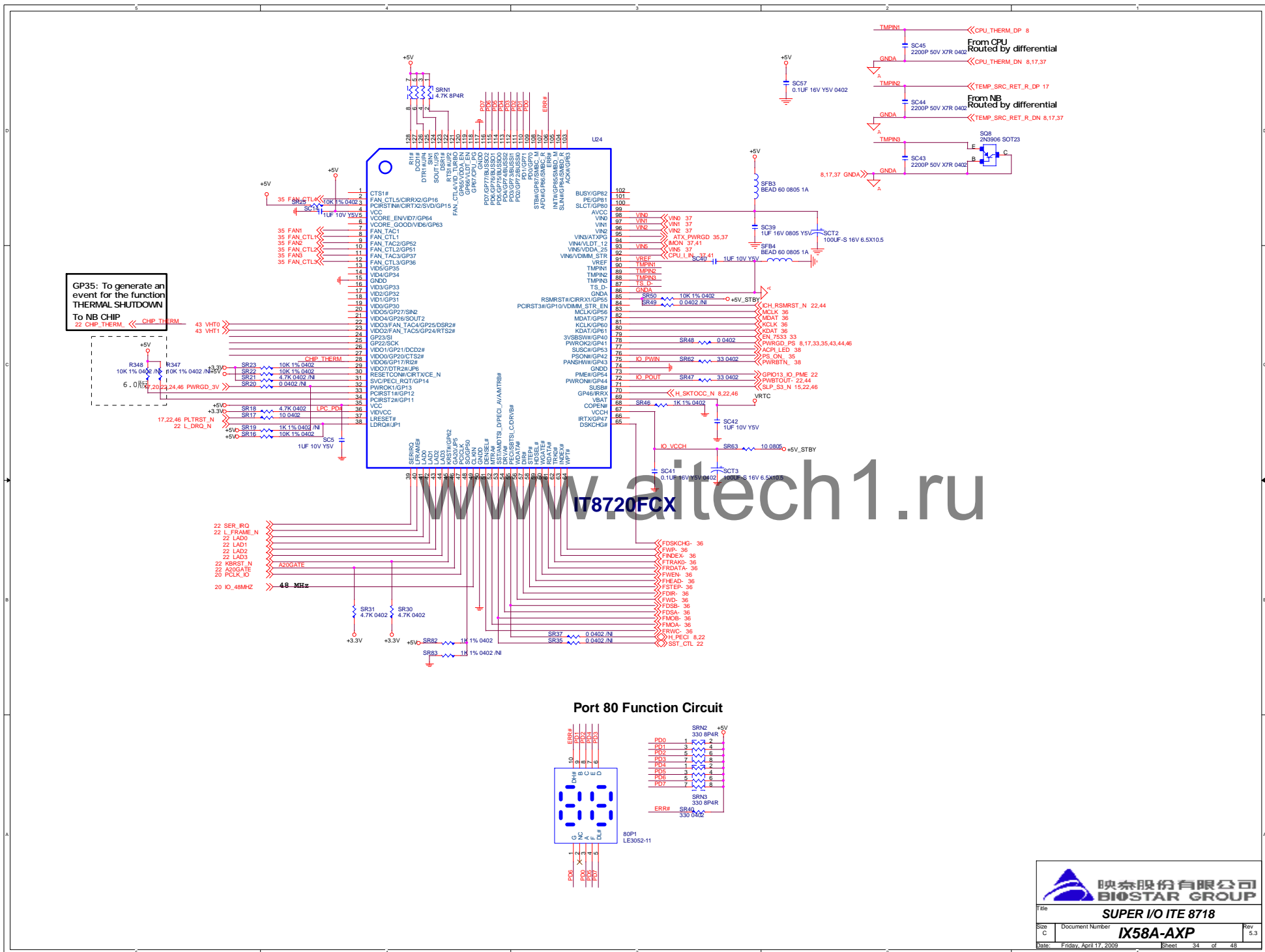


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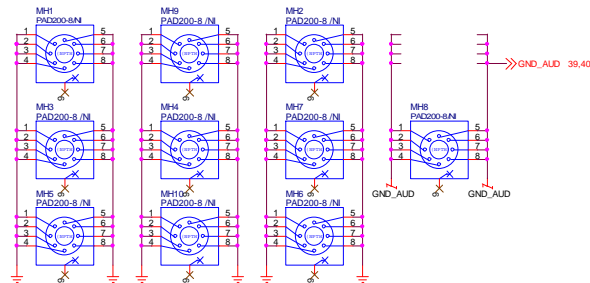
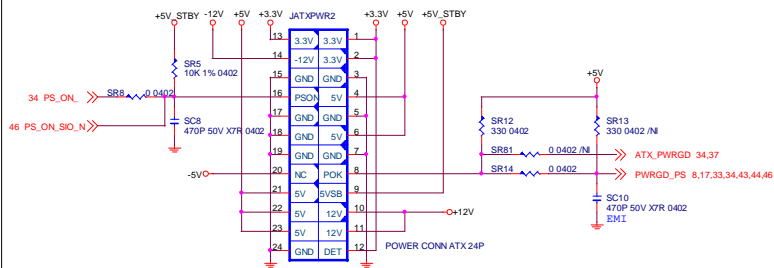


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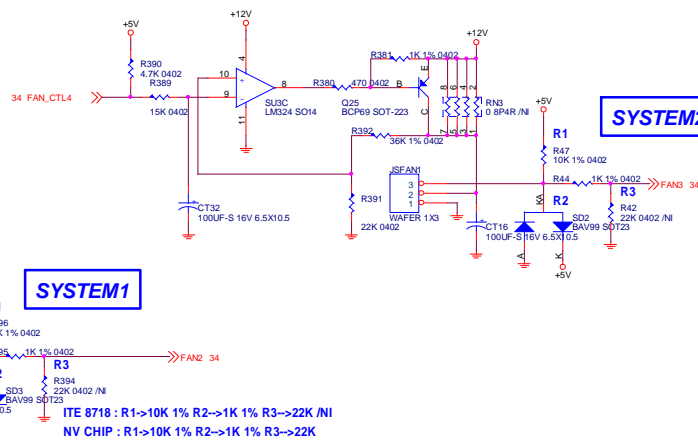
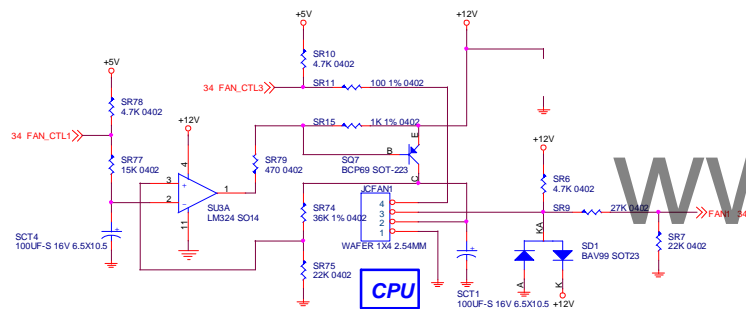
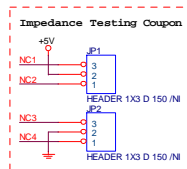


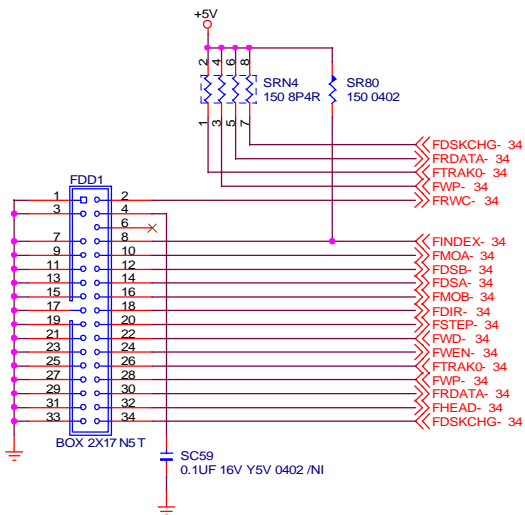


POWER CONNECTOR

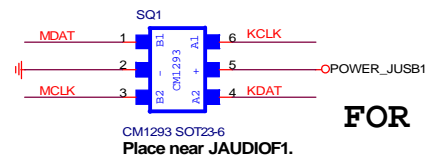


EMI POWER CONN DECOUPLING

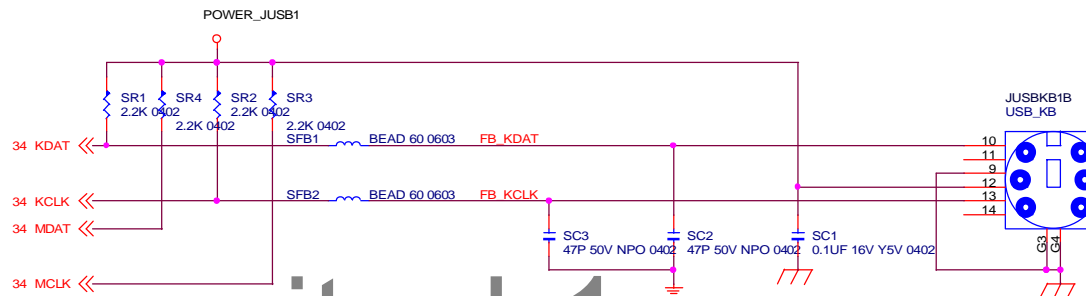




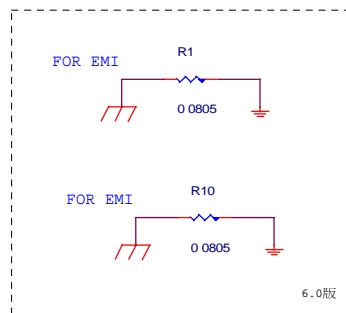
FLOPPY CONNECTOR



FOR KEYBOARD WITH MOUSE CONN.



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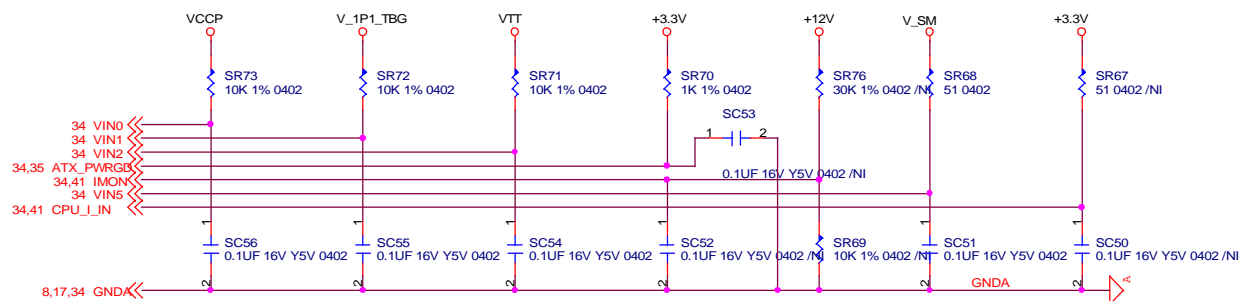
Title: **FDD / PS2 CONN**

Size: Custom Document Number: **IX58A-AXP** Rev: 5.3


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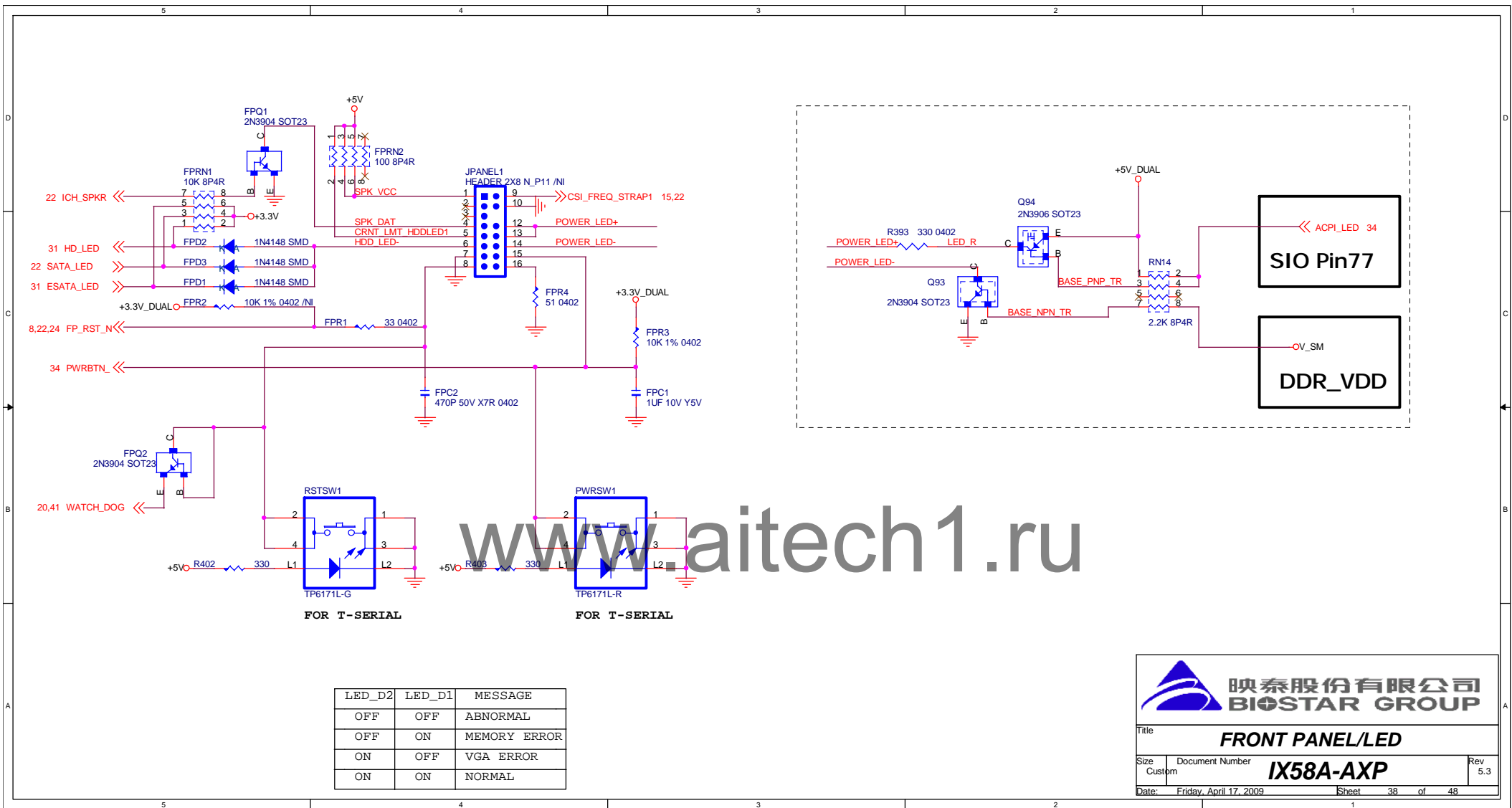
hardware monitor

Voltage Sensing



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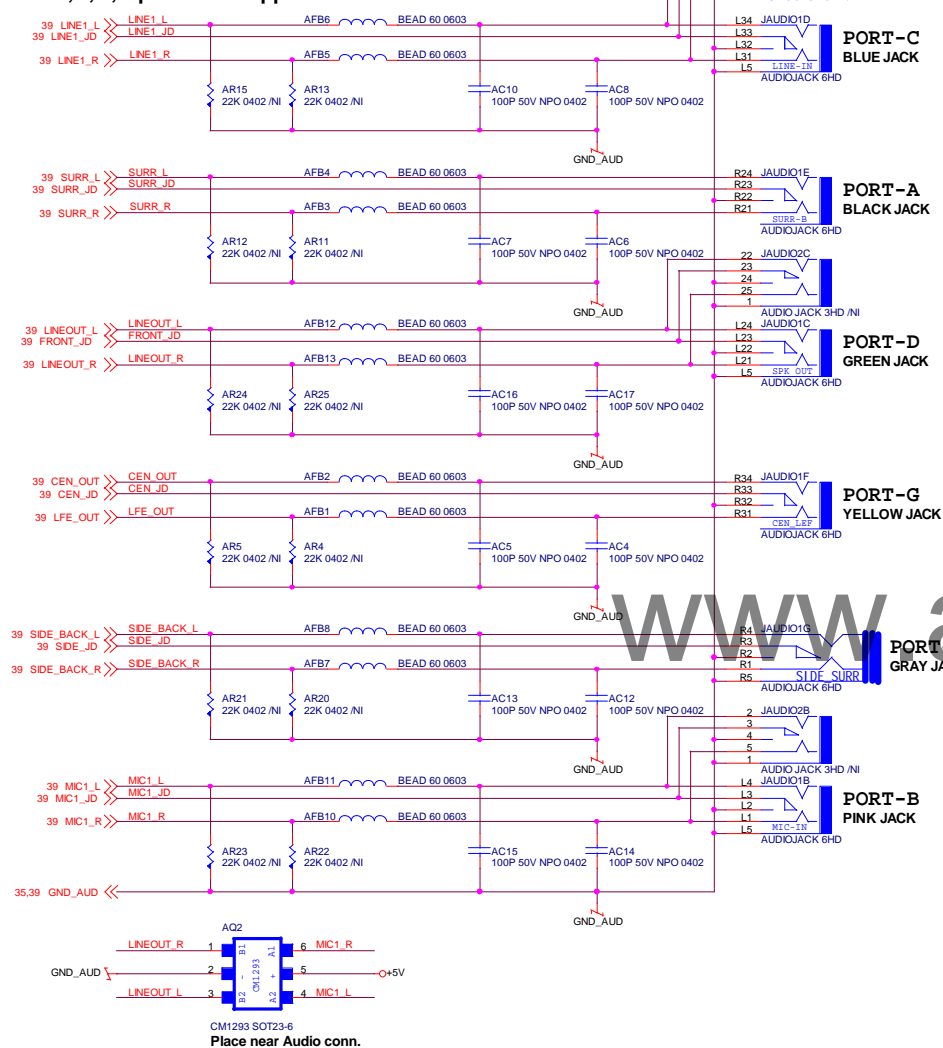
 映泰股份有限公司 BIOSSTAR GROUP			
Title H/W MONITOR			
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Date: Friday, April 17, 2009	Sheet	37 of 48	



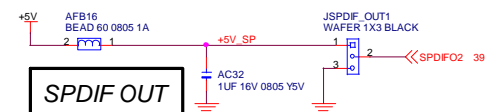
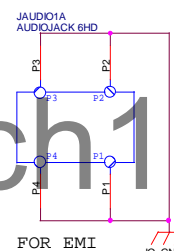
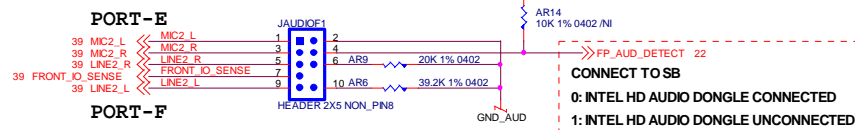
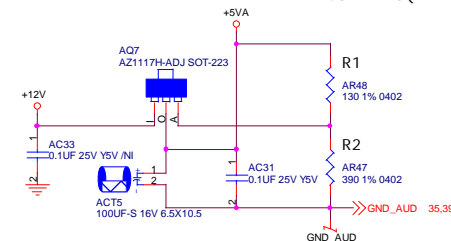
Rear Panel Onboard Analog I/O P07 : 3-Port & 6-Port Co-ley

ALC883/888:B,C,E,F ports support I/P & O/P function.

But A,D,G,H ports not support MIC function.

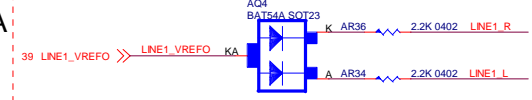


AUDIO ANALOG POWER $V_{OUT} = 1.25(1 + R2/R1)$

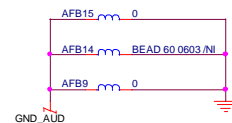
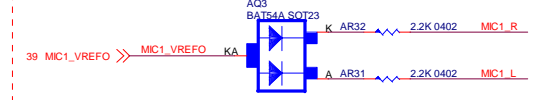


	ALC883/8	ALC861VD
HDA Interface	Yes	Yes
DAC	5 DAC	4 DAC
	7.1Ch + 2 Ch	7.1Ch
DAC SNR	95dB	90dB
GPIO for Vref	None	None
SPDIF-I	Yes	No
SPDIF-O	Yes	Yes
Re-Tasking	4-PORTS	Port D,E,F

FOR ALC883/888



FOR ALC88X/861



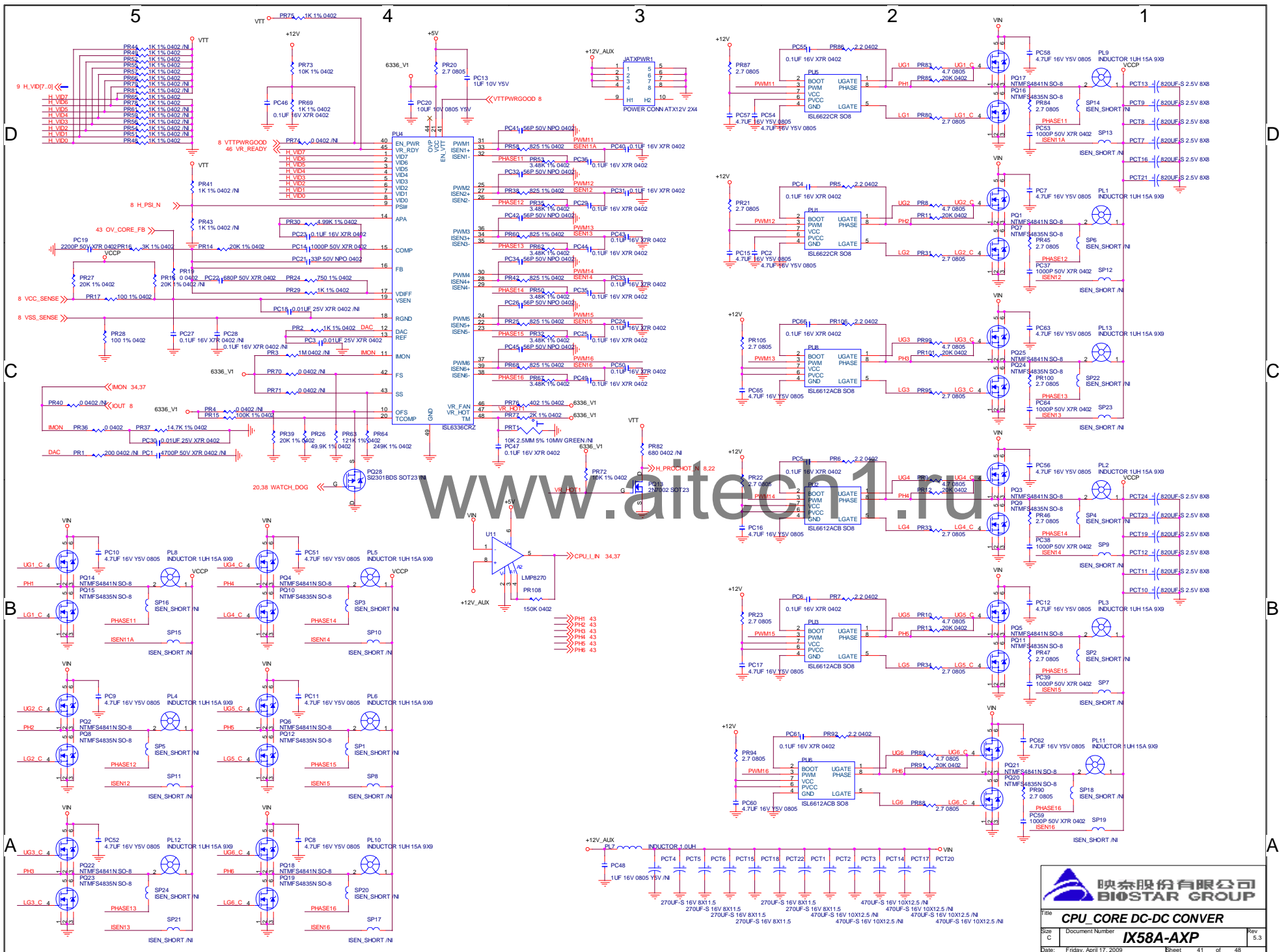
V0.51 Add EMI component CM1293 SOT23-6

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Title: **AUDIO CONNECTOR**

Size: Custom Document Number: **IX58A-AXP** Rev: 5.3

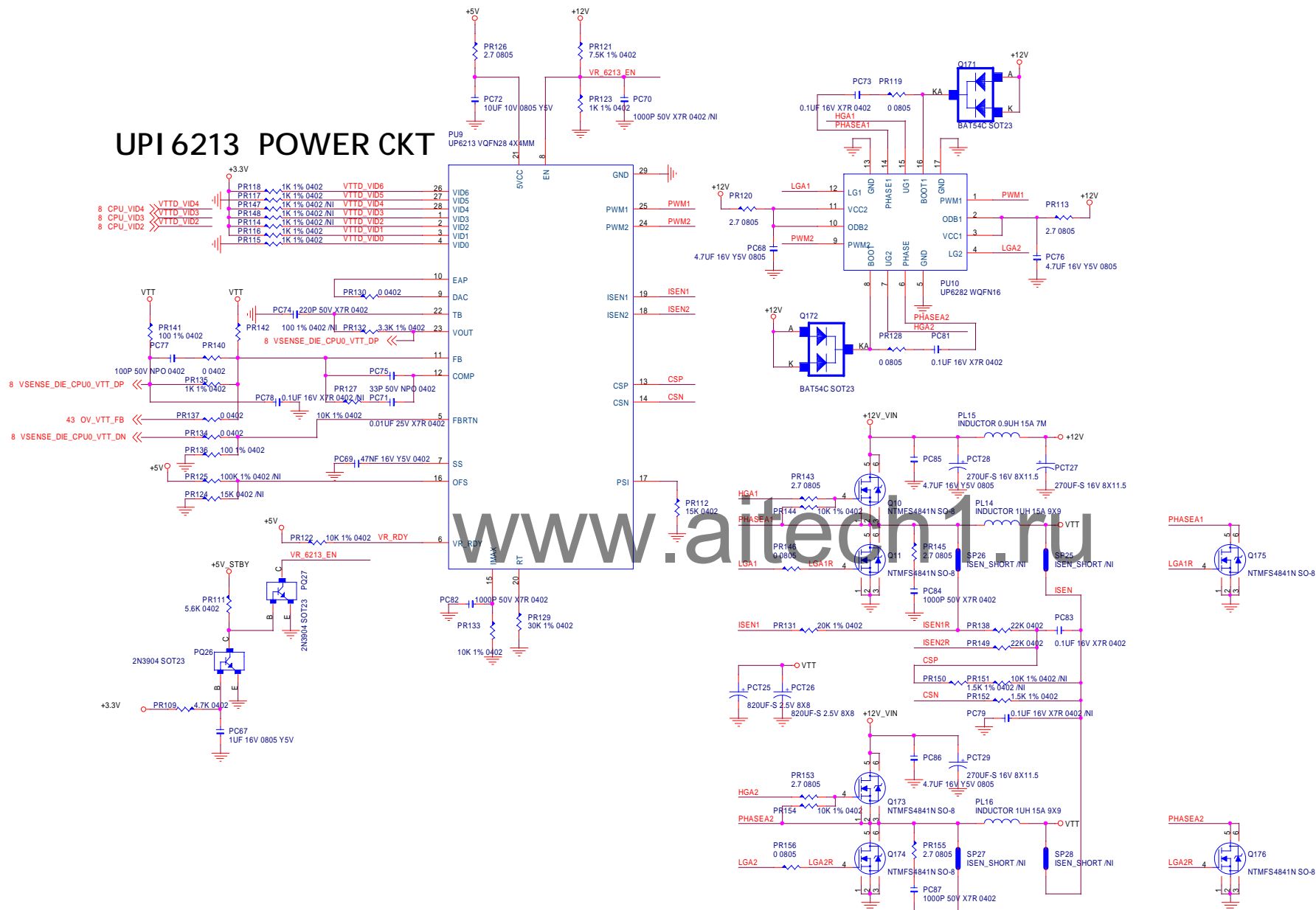
Date: Friday, April 17, 2009 Sheet: 40 of 48

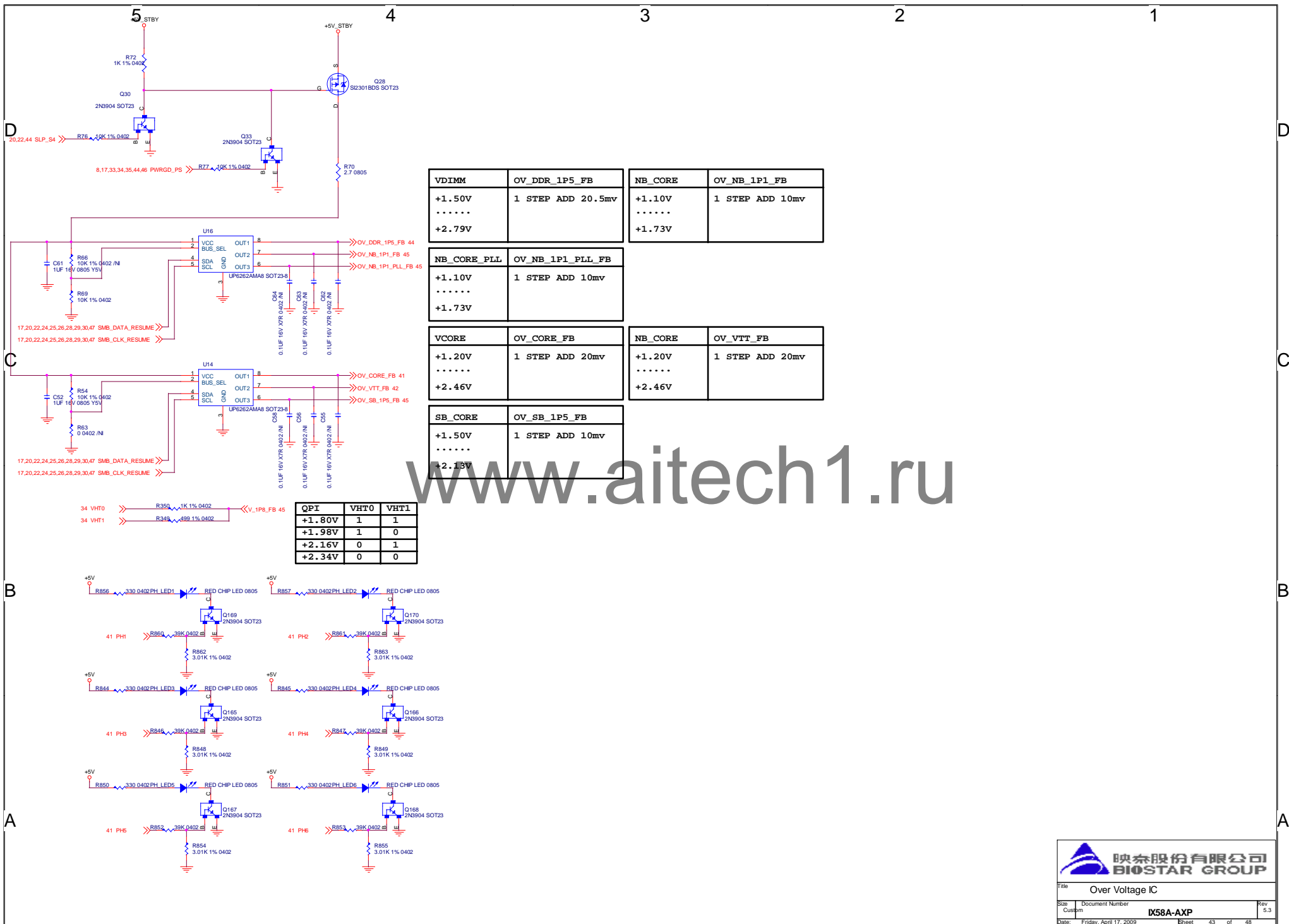


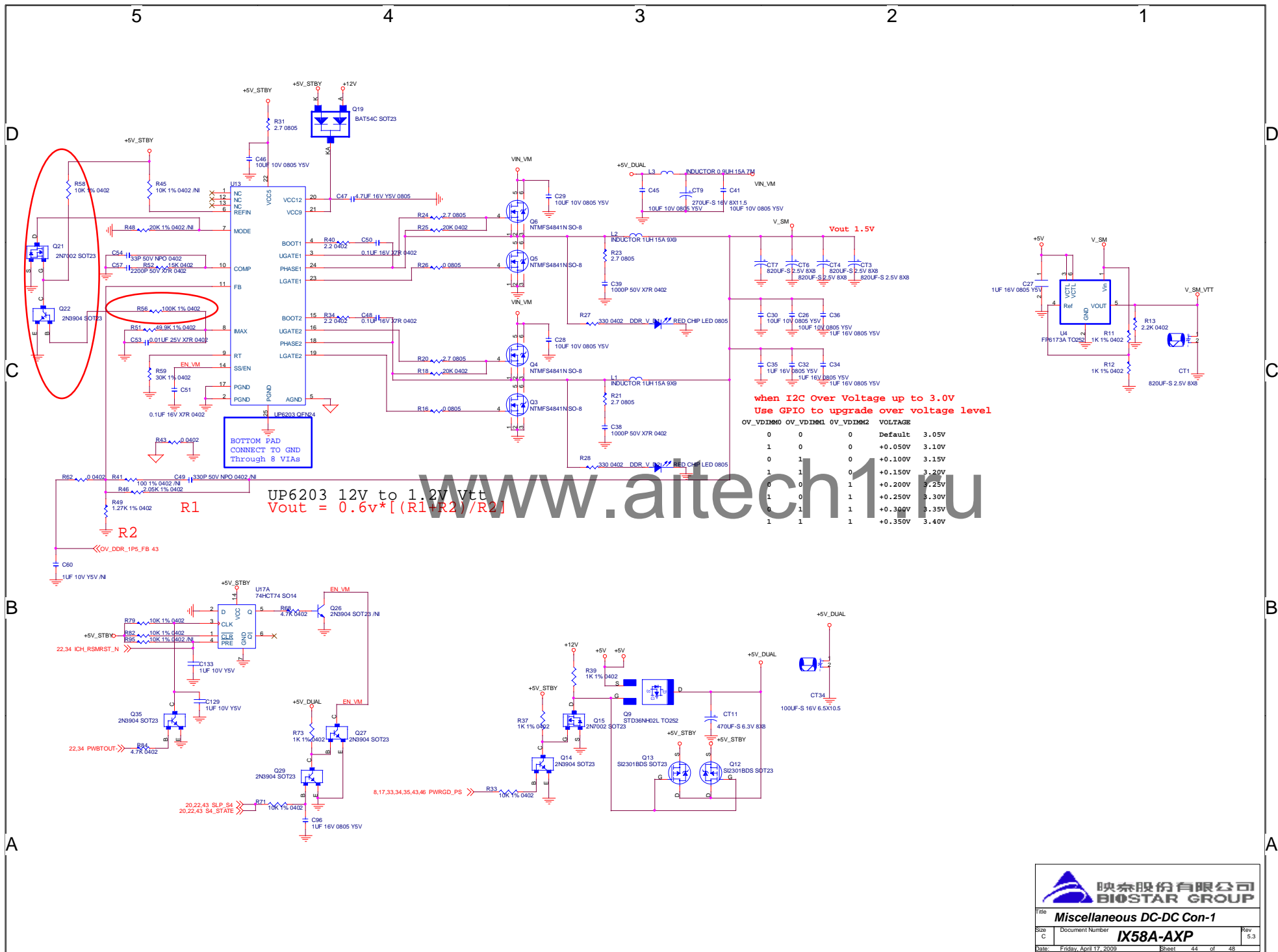
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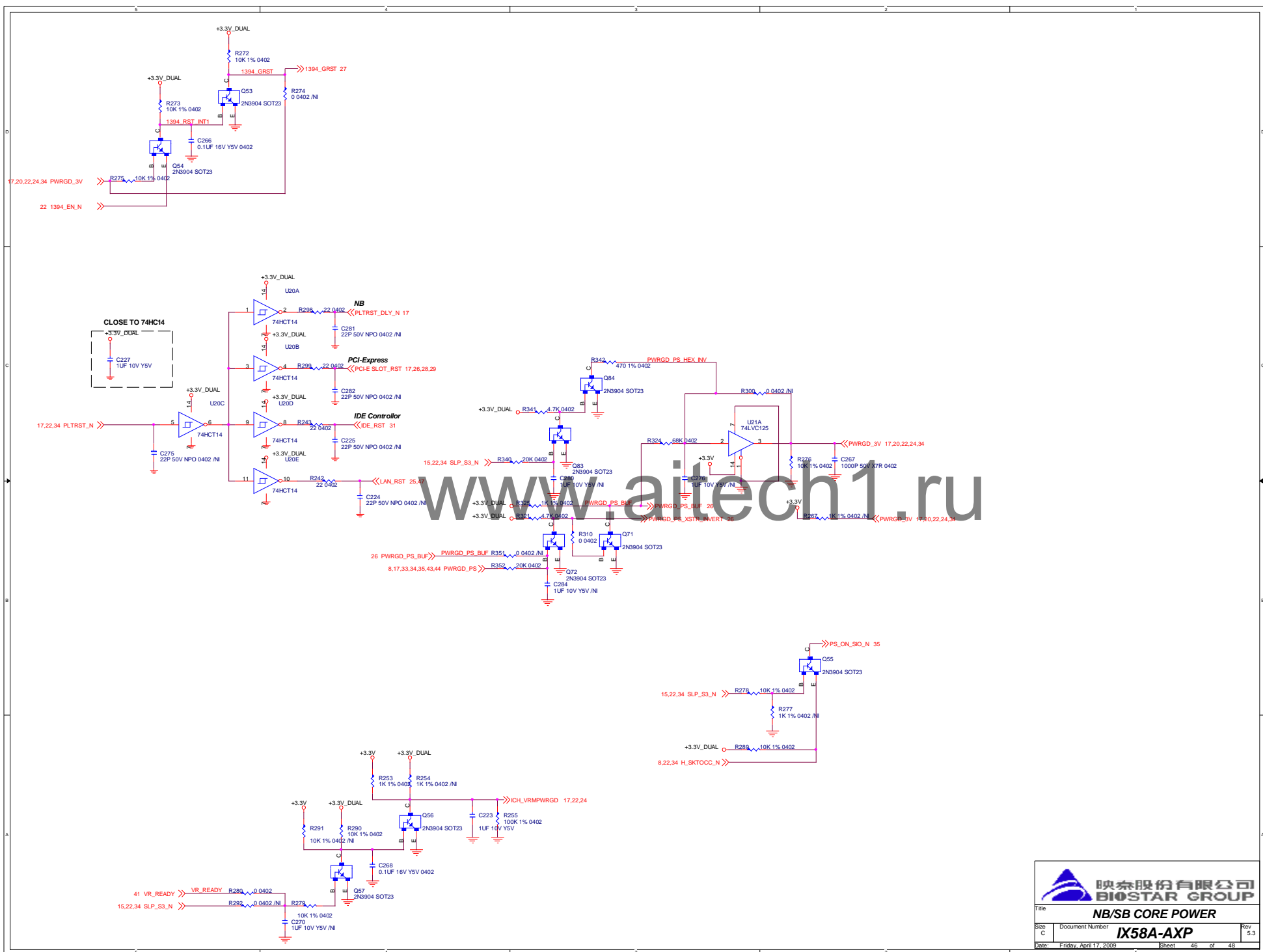
File: **CPU_CORE DC-DC CONVER**
Size: **IX58A-AXP**
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UPI 6213 POWER CKT









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Title NB/SB CORE POWER		
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A

B

C

D

E

Remove LFB1,LFB6 for RTL8111C

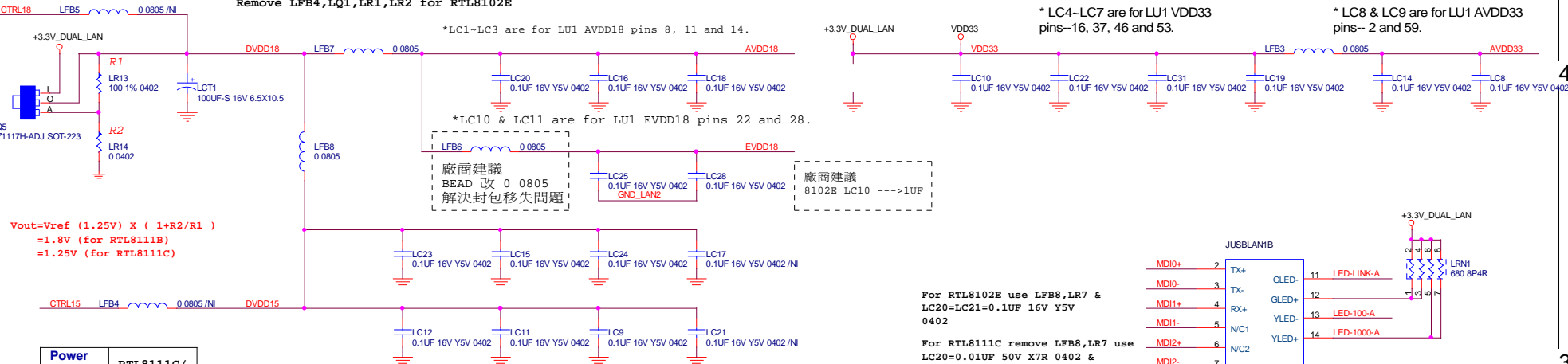
Remove LFB4,LQ1,LR1,LR2 for RTL8102E

*LC1~LC3 are for LU1 AVDD18 pins 8, 11 and 14.

*LC10 & LC11 are for LU1 EVDD18 pins 22 and 28.

*LC4~LC7 are for LU1 VDD33 pins--16, 37, 46 and 53.

*LC8 & LC9 are for LU1 AVDD33 pins-- 2 and 59.



3

3

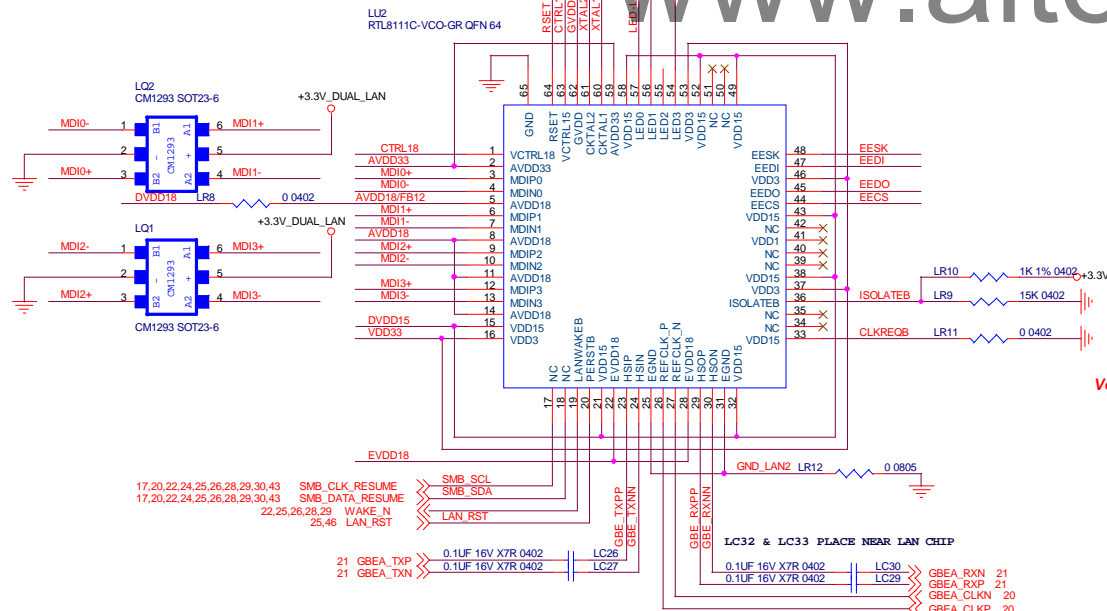
2

2

1

1

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Vout=Vref (1.25V) X (1+R2/R1)
=3.3V

For RTL8102E Remove LU2,LC24

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Title: RTL8111C/RTL8102E	
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A

B

C

D

E

